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Yasuyo SOGAWA, et al.

Group Art Unit:

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For: SEMICONDUCTOR INTEGRATED CIRCUIT

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Customer Window U.S. Patent & Trademark Office Randolph Building 401 Dulany Street Alexandria, VA 22314

Sir:

Further to the Information Disclosure Statement filed on April 18, 2006 wherein Japanese Patent Application No. 52-67741 was cited, filed herewith is a statement provided by applicants' Japanese patent attorney.

Respectfully Submitted,

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